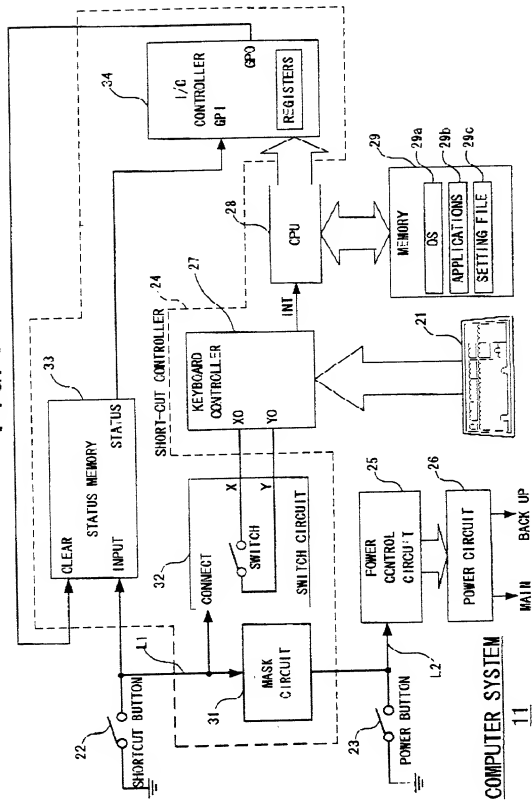


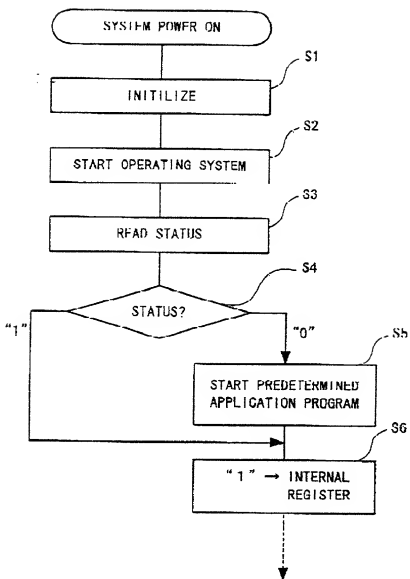
1/4
FIG. 1



COMPUTER SYSTEM

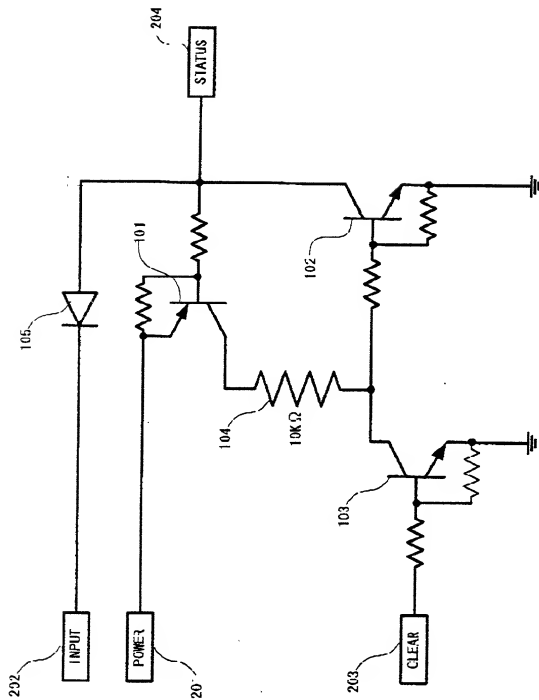
11

2/4
FIG. 2



3/4

FIG. 3



4/4 FIG. 4

CLEAR → "1"



FIG. 4A

INPUT	HI-Z	C
-------	------	---

FIG. 4B

MAIN POWER	OFF	ON
------------	-----	----

FIG. 4C

STATUS	HI-Z	0	1
--------	------	---	---

FIG. 4D

CLEAR	C	1
-------	---	---

FIG. 4E

DIGITAL TRANSISTOR 101	OFF	ON	OFF
------------------------	-----	----	-----

FIG. 4F

DIGITAL TRANSISTOR 102	OFF	ON	OFF
------------------------	-----	----	-----

FIG. 4G

DIGITAL TRANSISTOR 103	OFF	ON
------------------------	-----	----